

MV-OC9750-2525C is a Oven Controlled Crystal Oscillator (OCXO) . This OCXO is a Ultra Low G-Sense, Ultra Tight Stability, CMOS output and comes in a 9pin, Through Hole, Solder Seal 25.4x25.4mm package. This device contains an internal voltage regulator resulting in excellent power supply rejection ratio.

The device is qualified to meet the JEDEC standard for Pb-Free assembly and compliant to the RoHS directive.

#### Electrical Performance

Parameter	Min	Typ	Max	Units
<b>General</b>				
Output Frequency		100		MHz
Operating Temperature		-10/+70 to -40/+105		°C
Package Size		25.4 x 25.4 x 13.2		mm
<b>Frequency Stability</b>				
Stability over Temp		±2 to ±0.5		ppb
Initial Accuracy (at 25°C, 15min)			±50	ppb
Power Supply Stability (±5% Change)			±0.1	ppb
Load Stability (±10% Change)			±0.25	ppb
Aging / day			±0.1	ppb
Aging / year			±50	ppb
Aging - 20 years			±250	ppb
Warm Up (5 Min) - Ref to Stab @ 1hr / 25°C			±10	ppb
G-Sensitivity			0.05	ppb/g
<b>Supply</b>				
Supply Voltage (Vdd)		+5.0		V
Supply Current (Warm Up)			1050	mA
Power Consumption (Steady State @+25°C)			2.0	W
<b>Output</b>				
Output Signal		CMOS		
Output Level - Logic Low		+0.1	+0.4	V
Output Level - Logic High	+2.4	+3.3		V
Output Load		15		pF
Output Rise and Fall Time			6	nS
Duty Cycle	45	50	55	%
<b>Phase Noise &amp; Jitter</b>				
Phase Noise: (100 MHz)				
1 Hz offset		-61	-54	dBc/Hz
10 Hz offset		-89	-83	dBc/Hz
100 Hz offset		-107	-103	dBc/Hz
1kHz offset		-128	-124	dBc/Hz
10kHz offset		-133	-131	dBc/Hz
100kHz offset		-133	-130	dBc/Hz

#### Notes:

- 1 Warm Up: Stability referenced to frequency after 1 hour of operation at 25°C.
- 2 Initial tolerance specified at time of shipment and at nominal EFC
- 3 Long term Aging: may increase based on Operating Temperature range selected.

**Maximum Ratings**

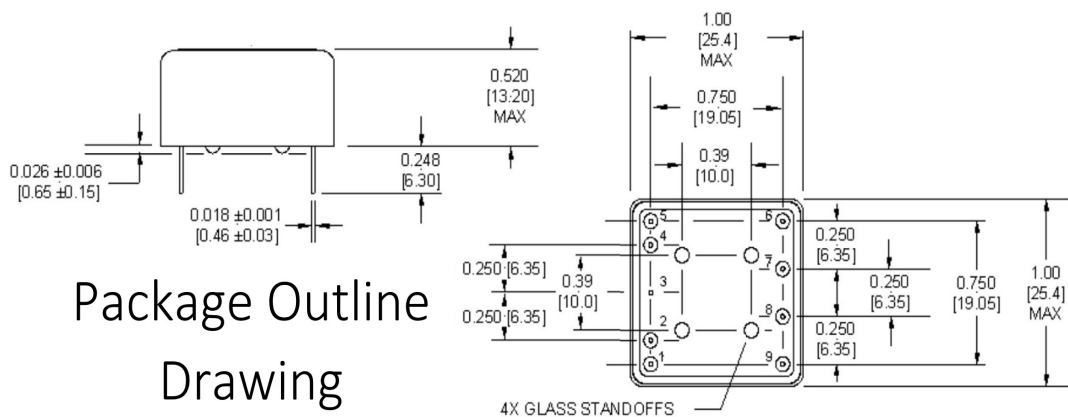
Storage Temp	-45°C to 95°C
Shock (MIL-STD-202, Method 213, Test Condition C)	Survive
Vibration (MIL-STD-202, Method 204, Condition A)	Survive

**Maximum Ratings Notes:**

- 1 Stresses in excess of the absolute maximum ratings can permanently damage the device.
- 2 Exposure to absolute maximum ratings for extended periods may adversely affect device reliability.

**Package Information**

Pin #	Function
Pin 1	OUT = Output
Pin 3	GND = Ground
Pin 9	Vdd = Supply Voltage
All Others	NC = Make No Connection



Package Outline  
Drawing

**Handling and Construction**

Package Construction	9pin, Through Hole, Solder Seal
RoHS compliance	100% ROHS 6 compliant
ESD, Human Body Model	500V
ESD, Charge Device Model	500V

**Ordering Information**

**MV-OC9750-2525C - A x x x - 100M0000**

OCXO, CMOS

① ② ③ ④

Frequency

25.4 x 25.4 x 13.2mm, 9 Pins

① Voltage

A: 5.0 V

② Temp Range

J: -10/+70 °C

H: -20/+70 °C

K: -40/+85 °C

L: -40/+105 °C

③ Temp Stability

N: ±2 ppb

Q: ±1.0 ppb

R: ±0.5 ppb

④ EFC

**Part Number Configuration Notes:**

- 1 Tightest temperature stability options available as at -10 to +70 °C. For tightest stability at wide temp ranges - please contact us.