

MV-OC911-3627E is a Oven Controlled Crystal Oscillator (OCXO) . This OCXO is a Standard OCXO, Euro Case, @ 12V, CMOS output and comes in a 5pin, Through Hole, Resistance Welded 36 x 27mm package. This device contains an internal voltage regulator resulting in excellent power supply rejection ratio.

The device is qualified to meet the JEDEC standard for Pb-Free assembly and compliant to the RoHS directive.

Electrical Performance

Parameter	Min	Typ	Max	Units
General				
Output Frequency		10		MHz
Operating Temperature		-10/+70 to -40/+85		°C
Package Size		36 x 27 x 12.7		mm
Frequency Stability				
Stability over Temp		±10 to ±2.5		ppb
Initial Accuracy (at 25°C, 15min)			±100	ppb
Power Supply Stability (±5% Change)			±0.5	ppb
Load Stability (±5% Change)			±0.5	ppb
Aging / day			±0.5	ppb
Aging / year			±50	ppb
Aging - 10 years			±300	ppb
Warm Up (10 Min) - Ref to Stab @ 1hr / 25°C			±10	ppb
Short Term (ADEV), $\tau = 1$ sec @ 10MHz			5e-11	
Supply				
Supply Voltage (Vdd)	11.4	+12	12.6	V
Supply Current (Warm Up)			400	mA
Power Consumption (Steady State @+25°C)			1.3	W
Tuning				
Electrical Frequency Control (EFC)		±500		ppb
Slope		Positive		
Control Voltage to reach Pull Range	0	+2.5	Vref	V
Vref - Reference Voltage	4.78	5	5.25	V
Linearity		10		%
Control Voltage Impedance	100			K Ω
Output				
Output Signal		CMOS		
Output Level - Logic Low			+0.4	V
Output Level - Logic High	+2.6			V
Output Load		15		pF
Output Rise and Fall Time			6	nS
Spurious			-60	dBc
Duty Cycle	45	50	55	%
Phase Noise & Jitter				
Phase Noise: (10 MHz)				
1 Hz offset		-95	-90	dBc/Hz
10 Hz offset		-125	-120	dBc/Hz
100 Hz offset		-146	-143	dBc/Hz
1kHz offset		-155	-152	dBc/Hz
10kHz offset		-158	-155	dBc/Hz
100kHz offset		-158	-155	dBc/Hz

Notes:

- 1 Warm Up: Stability referenced to frequency after 1 hour of operation at 25°C.
- 2 Initial tolerance specified at time of shipment and at nominal EFC

Maximum Ratings

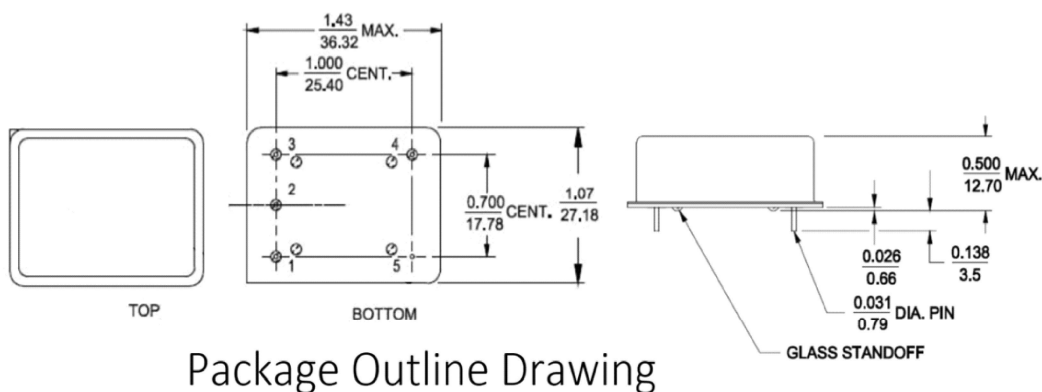
Storage Temp	-55°C to 105°C
Shock (MIL-STD-202, Method 213, Test Condition J)	30g, 11ms, half-sine (non-operating)
Vibration (MIL-STD-202, Method 201)	0.06" Total p-p, 10 to 55 Hz (non-operating)
Humidity (MIL-STD-202, Method 103, Test Condition A)	95% RH @ +40°C, non-condensing, 240 hours

Maximum Ratings Notes:

- 1 Stresses in excess of the absolute maximum ratings can permanently damage the device.
- 2 Exposure to absolute maximum ratings for extended periods may adversely affect device reliability.

Package Information

Pin #	Function
Pin 1	Vc = Control Voltage or NC
Pin 2	Reference Voltage (Vref) or NC
Pin 3	Vdd = Supply Voltage
Pin 4	OUT = Output
Pin 5	GND = Ground



Package Outline Drawing

Handling and Construction

Package Construction	5pin, Through Hole, Resistance Welded
RoHS compliance	100% ROHS 6 compliant
ESD, Human Body Model	500V
ESD, Charge Device Model	500V

Ordering Information
MV-OC911-3627E -

OCXO, CMOS

36 x 27 x 12.7mm, 5 Pins

Z x x Z

① ② ③ ④

- 10M00000

Frequency

① Voltage

Z: 12 V

② Temp Range

J: -10/+70 °C

H: -20/+70 °C

K: -40/+85 °C

③ Temp Stability

J: ±10 ppb

L: ±5 ppb

M: ±2.5 ppb

④ EFC

Z: ±500 ppm

Part Number Configuration Notes:

- 1 Tightest temperature stability options available as at narrowest temperature range listed above as standard.
- 2 For tighter stabilities at wider temp ranges - please contact us to confirm feasibility.